

2025 RELEASE UNDER E.O. 14176

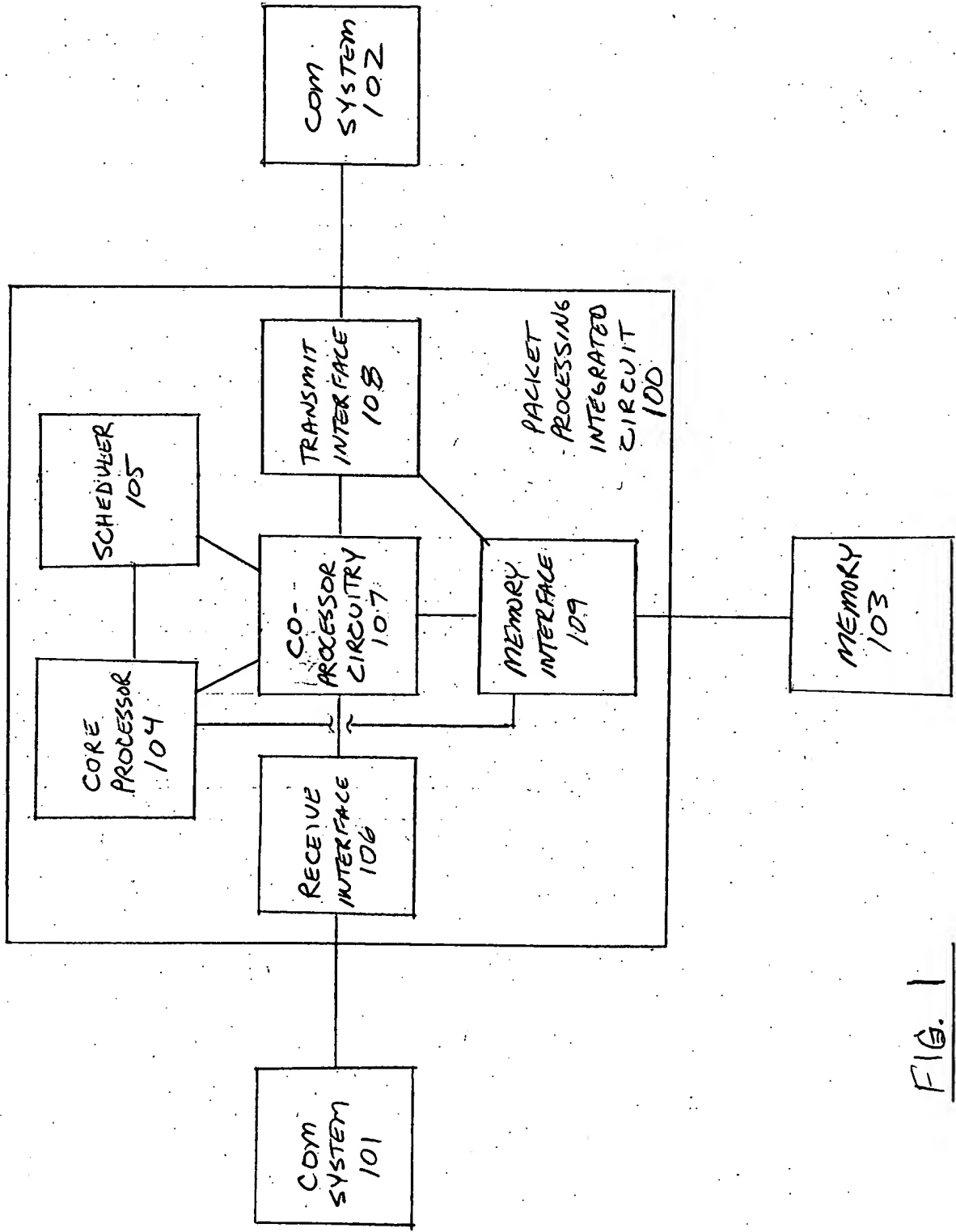


FIG. 1

STAGES
CONTEXT
FLUSHING

STAGE #4
SOFTWARE
APPLICATION

STAGE #3
PRIORITY QUEUING

STAGE 2
CONTEXT
FETCH

STAGE #1 CONTEXT RESOLUTION

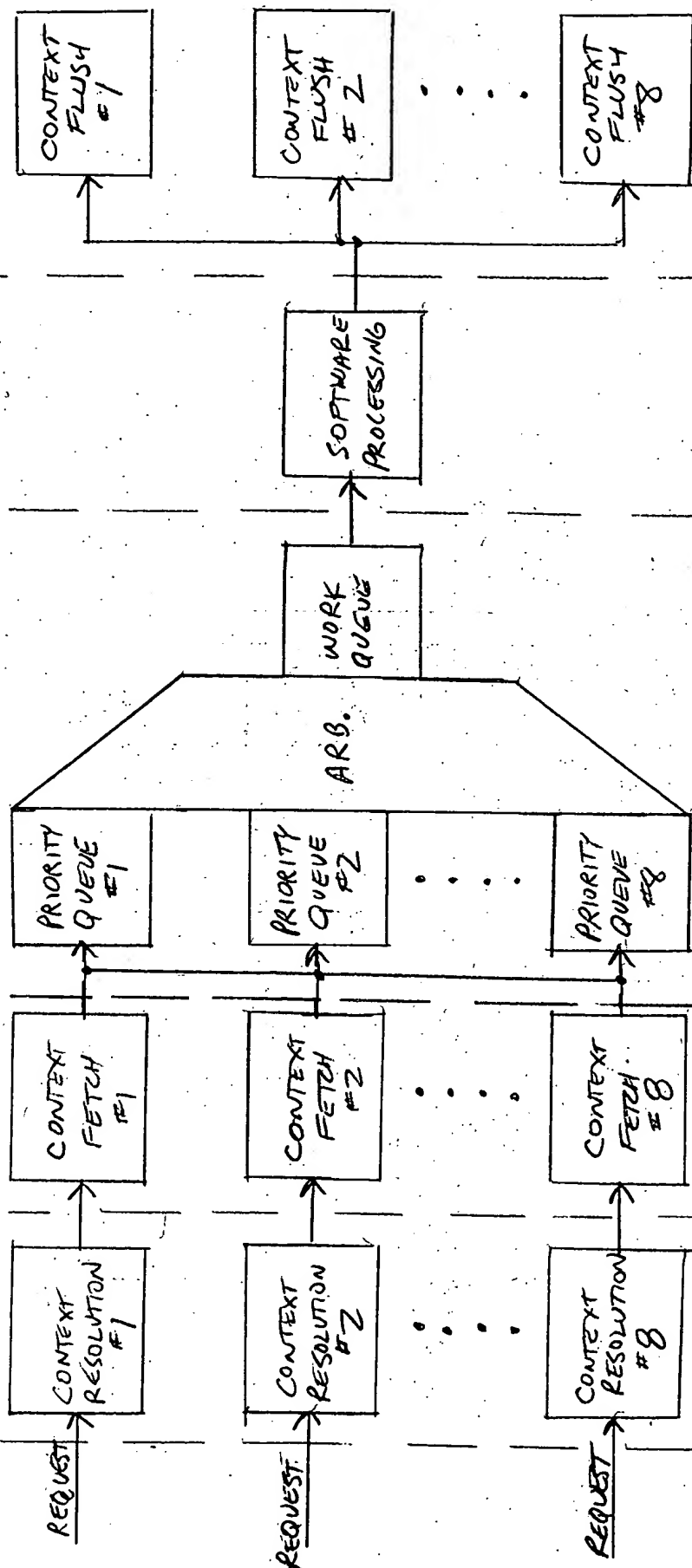


FIG. 2.

TO CORE PROCESSOR 104

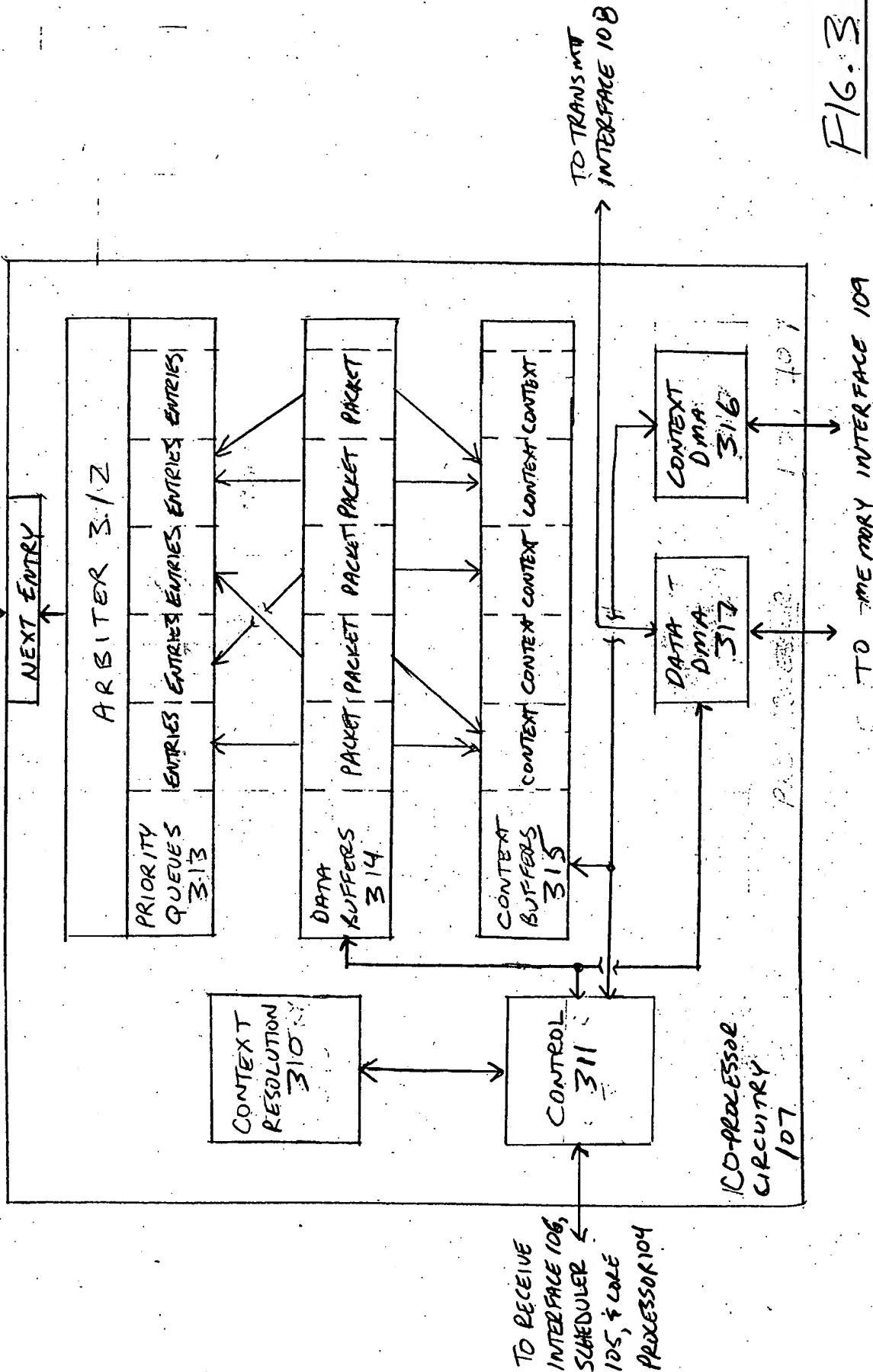


FIG. 3



22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS

START

STORE PACKET IN DATA BUFFER
AND CORRELATE TO CHANNEL
DESCRIPTOR

VALID
CHANNEL DESCRIPTOR IN A
CONTEXT BUFFER?

Y
ASSOCIATE DATA BUFFER WITH
CONTEXT BUFFER. INCREMENT
IN-USE COUNT.

N
RETRIEVE CHANNEL DESCRIPTOR
AND STORE IN CONTEXT BUFFER.
ASSOCIATE DATA BUFFER WITH
CONTEXT BUFFER. SET
IN-USE COUNT TO ONE. SET
VALIDITY BIT

TRANSFER PACKET FROM DATA
BUFFER. DECREMENT IN-USE COUNT.

IN-USE COUNT = 0
?

Y
TRANSFER CHANNEL DESCRIPTOR
TO MEMORY. CLEAR VALIDITY
BIT.

END

FIG. 4

00640260-001600

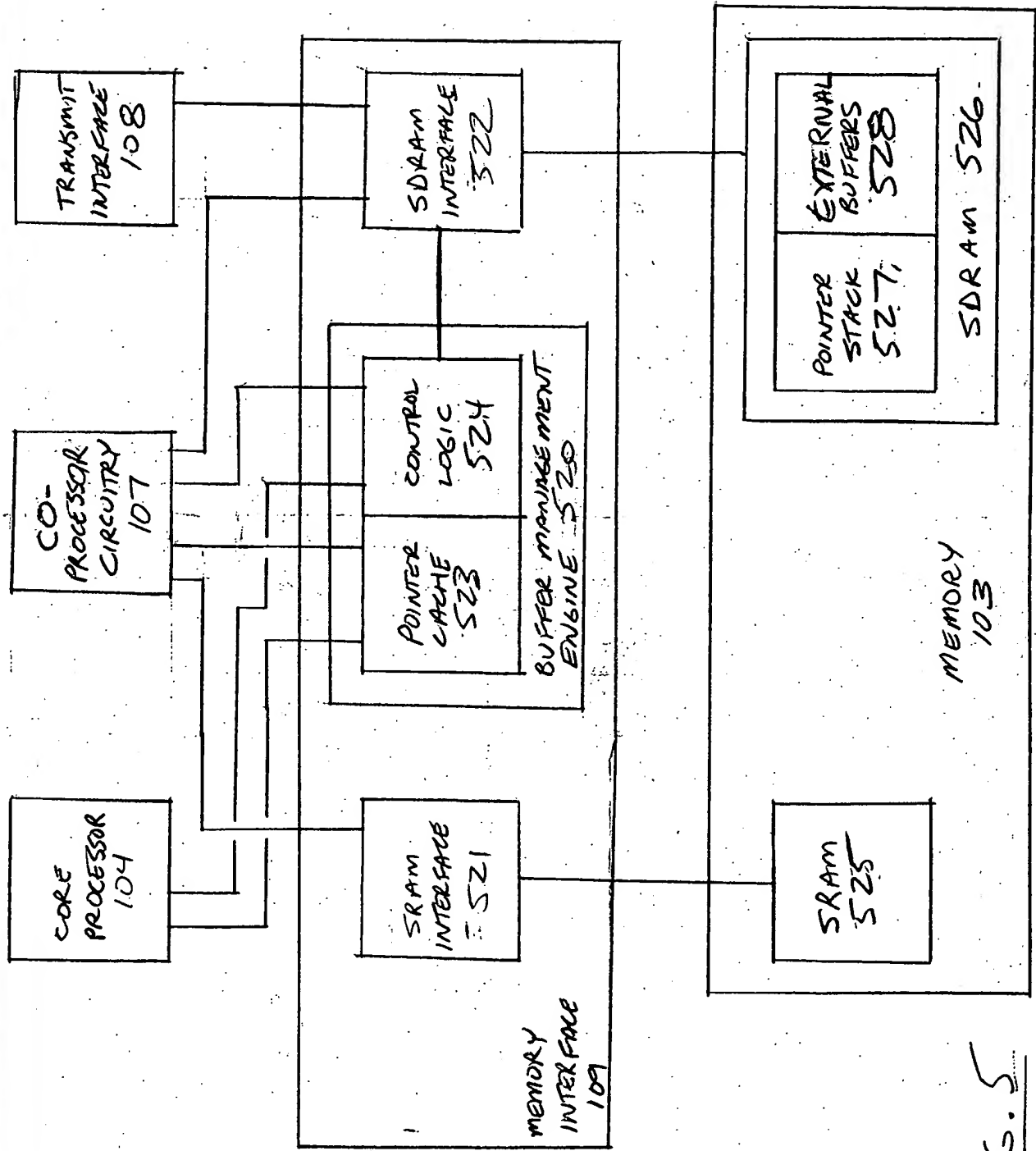


FIG. 5

CLASS	EXTERNAL BUFFERS	POOL	TYPE	FAIL OVER
1	1-500	A	STATIC	—
2	501-1000	A	EXCL.	3
3	1001-1500	A	SHARED	—
4	1501-2000	A	EXCL.	6
5	2001-2500	A	EXCL.	6
6	2501-3000	A	SHARED	—
7	3001-3500	A	STATIC	—
8	3501-4000	A	STATIC	—
9	4001-4500	B	STATIC	—
10	4501-5000	B	EXCL.	11
11	5001-5500	B	SHARED	—
12	5501-6000	B	EXCL.	14
13	6001-6500	B	EXCL.	14
14	6501-7000	B	SHARED	—
15	7001-7500	B	STATIC	—
16	7501-8000	B	STATIC	—

FIG. 6

105

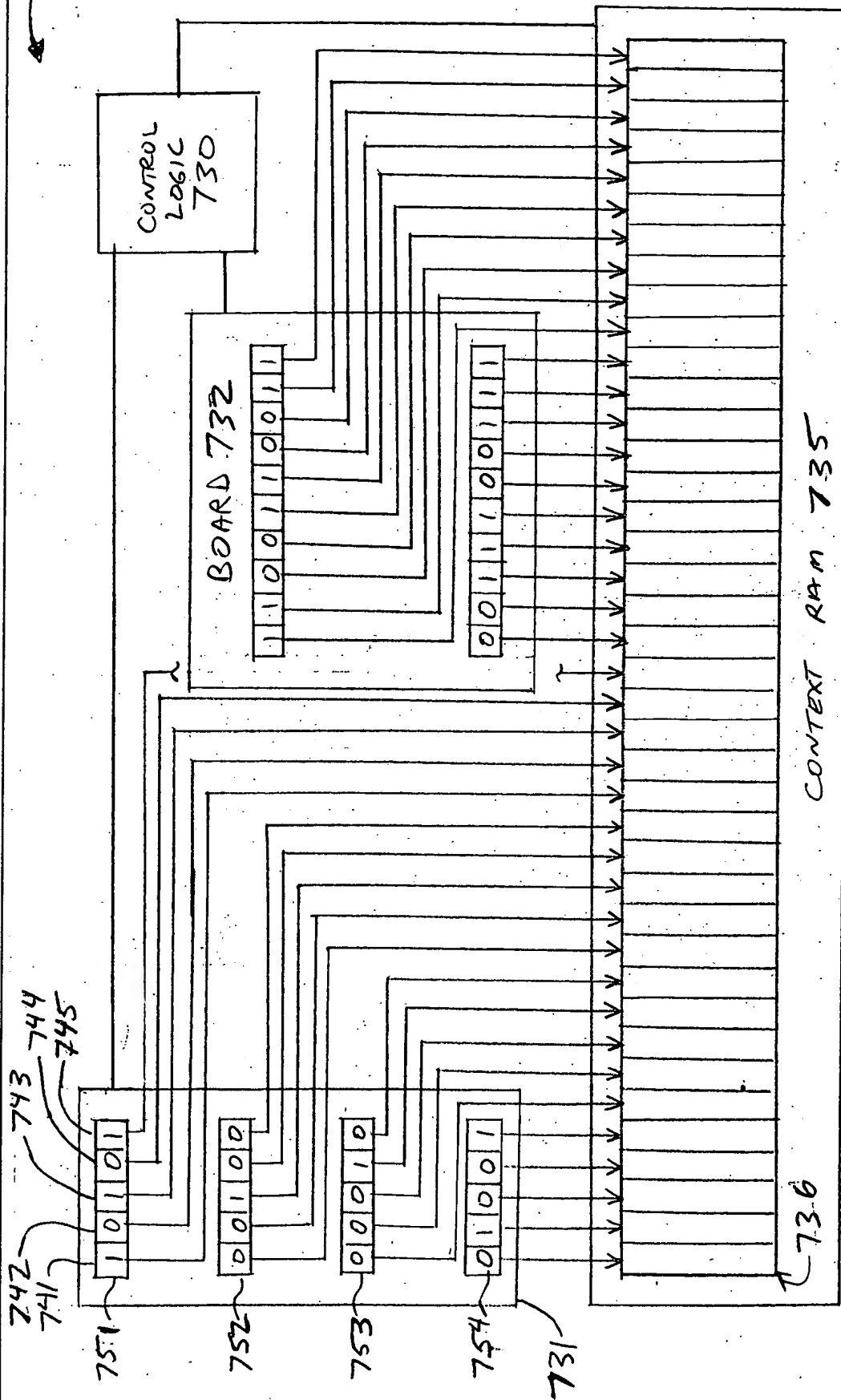


FIG. 7

09640260.081600

742 743 744 745

741

BOARD 731

TIME PERIOD	1	2	3	4	5
PRIORITY #1	1	0	1	0	1
PRIORITY #2	0	0	1	0	0
PRIORITY #3	0	0	0	1	0
PRIORITY #4	0	1	0	0	1

751 752 753 754

860

F16-8

TIME PERIOD	1	2	3	4	5
PRIORITY #1	1	0	1	0	1
PRIORITY #2	0	0	1	0	0
PRIORITY #3	0	0	0	1	0
PRIORITY #4	0	1	0	0	1

860

F16.9